

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Toshiyuki ARITA**

Serial Number: **Not Yet Assigned**

Filed: **August 6, 2003**

For: **SEMICONDUCTOR APPARATUS FABRICATION METHOD**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

August 6, 2003

Sir:

In compliance with 37 CFR 1.56, Applicant calls to the attention of the Patent and Trademark

Office the references listed on the attached PTO-1449.

A copy of each of the references are enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge

Deposit Account No. 01-2340.

Respectfully submitted,

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PATENT TRADEMARK OFFICE

Enclosures: PTO-1449; References (4)

INFORMATION DISCLOSURE STATEMENT PTO-1449	Atty. Docket No. 030905	Serial No. New Application
	Applicant(s): Toshiyuki ARITA	
	Filing Date: August 6, 2003	Group Art Unit: Not Yet Assigned

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					

FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Translation (Yes or No)
_____ AE 11-145031	05/28/99	Japan	Yes-Abstract/Discussed in the specification
_____ AF 2001-332484	11/30/01	Japan	Yes-Abstract/Discussed in the specification
_____ AG 7-45510	02/14/95	Japan	Yes-Abstract
_____ AH 62-215265	09/21/87	Japan	Yes-Abstract
_____ AI			

OTHER DOCUMENTS

_____	AJ			
_____	AK			
<table border="1" style="width: 100%;"> <tr> <td style="width: 50%;">Examiner</td> <td style="width: 50%;">Date Considered</td> </tr> </table>			Examiner	Date Considered
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